SCHEME OF TEACHING & EXAMINATION ELECTRONICS & COMMUNICATION ENGINEERING III SEMESTER (COMMON TO EC/TC/ML)

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Subject Code** | **Title** | **Teach ing Dept.** | **Teaching hours/week** | | **Examination** | | | |
| **Theory** | **Practical** | **Duration** | **I. A** | **Theory/ Practical** | **Total Marks** |
| **10MAT - 31** | **Engg. Mathematics - III** | **Mat** | **04** |  | **03** | **25** | **100** | **125** |
| **10ES – 32** | **Analog Electronic Ckts** | **@** | **04** |  | **03** | **25** | **100** | **125** |
| **10ES – 33** | **Logic Design** | **@** | **04** |  | **03** | **25** | **100** | **125** |
| **10ES – 34** | **Network Analysis** | **@** | **04** |  | **03** | **25** | **100** | **125** |
| **10IT– 35** | **Electronic Instrumentation** | **@** | **04** |  | **03** | **25** | **100** | **125** |
| **10ES – 36** | **Field Theory** | **@** | **04** |  | **03** | **25** | **100** | **125** |
| **10ESL – 37** | **Analog Electronics Lab** | **@** |  | **03** | **03** | **25** | **50** | **75** |
| **10ESL – 38** | **Logic Design Lab** | **@** |  | **03** | **03** | **25** | **50** | **75** |
|  |  | **Total** | **24** | **06** | **24** | **200** | **700** | **900** |

# 1

SCHEME OF TEACHING & EXAMINATION ELECTRONICS & COMMUNICATION ENGINEERING IV SEMESTER (COMMON TO EC/TC/ML)

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Subject Code** | **Title** | **Teach Dept.** | **Teaching hours/week** | | **Examination** | | | |
| **Theory** | **Practical** | **Duration** | **I. A** | **Theory/ Practical** | **Total Marks** |
| **10MAT - 41** | **Engg. Mathematics – IV** | **Mat** | **04** |  | **03** | **25** | **100** | **125** |
| **10ES– 42** | **Microcontrollers** | **@** | **04** |  | **03** | **25** | **100** | **125** |
| **10ES – 43** | **Control Systems** | **@** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC – 44** | **Signals & Systems** | **@** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC– 45** | **Fundamentals of HDL** | **@** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC – 46** | **Linear ICs & Applications** | **@** | **04** |  | **03** | **25** | **100** | **125** |
| **10ESL – 47** | **Microcontrollers Lab** | **@** |  | **03** | **03** | **25** | **50** | **75** |
| **10ECL – 48** | **HDL Lab** | **@** |  | **03** | **03** | **25** | **50** | **75** |
|  |  | **Total** | **24** | **06** | **24** | **200** | **700** | **900** |

**Note : @** indicates concerned discipline. **ES ( for theory) & ECL ( for Lab)** in the subject code indicates that the subject is common to electrical and electronics stream consisting of **EE/EC/IT/TC/ML/BM branches** of engineering

# 2

FIFTH SEMESTER Common to EC/TE

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Subject Code** | **Title** | **Teaching Departm ent** | **Teaching hours/week** | | **Examination** | | | |
|  | **Theory** | **Practical** | **Duration** | **I. A** | **Theory/ Practical** | **Total Marks** |
| **10AL - 51** | **Management &**  **Entrepreneurship** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC– 52** | **Digital signal Processing** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC 53** | **Analog Communication** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10TE - 54** | **Trans. Lines & Waveguides** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC-55** | **Digital Switching Systems** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC – 56** | **Fundamentals of CMOS VLSI** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10ECL – 57** | **DSP Lab** | **EC** |  | **03** | **03** | **25** | **50** | **75** |

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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **10ECL - 58** | **Analog Communication Lab + LIC Lab** | **EC** |  | **03** | **03** | **25** | **50** | **75** |
|  |  | **Total** | **24** | **06** | **24** | **200** | **700** | **900** |

SIXTH SEMESTER for TE Branch

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Subject Code** | **Title** | **Teaching Dept.** | **Teaching hours/week** | | **Examination** | | | |
| **Theory** | **Practical** | **Duration** | **I. A** | **Theory/ Practical** | **Total Marks** |
| **10EC/TE– 61** | **Digital Communication** | EC | **04** |  | **03** | **25** | **100** | **125** |
| **10EC/TE – 62** | **Microprocessors** | EC | **04** |  | **03** | **25** | **100** | **125** |
| **10EC/TE - 63** | **Antennas & Propagation** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC/TE - 64** | **Microwaves & Radar** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC/TE - 65** | **Information theory and Coding** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC – 66X** | **Elective-(Group-A)** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10TEL – 67** | **Microwave & Antenna Lab** | **EC** |  | **03** | **03** | **25** | **50** | **75** |

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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **10TEL - 68** | **Microprocessors Lab** | **EC** |  | **03** | **03** | **25** | **50** | **75** |
|  |  | **Total** | **24** | **06** | **24** | **200** | **700** | **900** |

Electives -1(Group A)

|  |  |  |  |
| --- | --- | --- | --- |
| **10EC661** | **Programming in C ++** | **10EC664** | **Adaptive Signal Processing** |
| **10EC662** | **Radio Frequency Integrated Circuits** | **10EC665** | **Modern Control theory** |
| **10EC663** | **Random Processes** | **10EC666** | **Digital System Dsign Using Verilog** |

SEVENTH SEMESTER for TE Branch

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Subject Code** | **Title** | **Teaching Departme nt** | **Teaching hours/week** | | **Examination** | | | |
| **Theory** | **Practical** | **Duration** | **I. A** | **Theory/ Practical** | **Total Marks** |
| **10EC/TE/-71** | **Computer communication Network** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC/TE– 72** | **Optical Fiber Communication** | **EC** | **04** |  | **03** | **25** | **100** | **125** |

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| **10EC/TE – 73** | **Wireless Communication** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC/TE - 74** | **DSP Algorithms & Architecture** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10TC -75X** | **Elective -2 (Group B)** |  | **04** |  | **03** | **25** | **100** | **125** |
| **10TC – 76X** | **Elective-3 (Group C)** |  | **04** |  | **03** | **25** | **100** | **125** |
| **10TEL – 77** | **Advanced Communication** |  |  | **03** | **03** | **25** | **50** | **75** |
| **10TEL - 78** | **CCN LAB** |  |  | **03** | **03** | **25** | **50** | **75** |
|  |  | **Total** | **24** | **06** | **24** | **200** | **700** | **900** |

Electives -2(Group B)

|  |  |  |  |
| --- | --- | --- | --- |
| **10EC751** | **Operating Systems** | **10EC754** | **Image Processing** |
| **10EC752** | **Digital Signal Compression** | **10EC755** | **Video Engineering** |
| **10EC753** | **Artificial Neural Network** | **10EC756** | **Micro & Smart Systems** |

Electives -3(Group C)

|  |  |  |  |
| --- | --- | --- | --- |
| **10EC761** | **Data Structures using C++** | **10EC764** | **Wavelet Transforms** |
| **10TC762** | **Real Time Systems** | **10EC765** | **Embedded System Design** |
| **10EC763** | **Pattern Recognition** | **10EC766** | **Speech Processing** |

EIGTH SEMESTER for TE Branch

# 6

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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Subject Code** | **Title** | **Teaching Departme nt** | **Teaching hours/week** | | **Examination** | | | |
| **Theory** | **Practical** | **Duration** | **I. A** | **Theory/ Practical** | **Total Marks** |
| **10TE-81** | **Optical Networking** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10C/TE– 82** | **GSM** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC/TE -** | **Elective - 4(Group D)** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10EC/TE –** | **Elective- 5(Group E)** | **EC** | **04** |  | **03** | **25** | **100** | **125** |
| **10TE – 85** | **Project Work** | **EC** | - | **06** | **03** | **50** | **100** | **150** |
| **10TE - 86** | **Seminar** | **EC** | - | - | - | **50** | - | **50** |
|  |  | **Total** | **16** | **06** | **24** | **200** | **500** | **700** |

Electives -4(Group D)

|  |  |  |  |
| --- | --- | --- | --- |
| **10EC831** | **Distributed Syste ms** | **10EC834** | **Mobile Computing** |
| **10EC832** | **Network Security** | **10EC835** | **High performance computer networks** |
| **10EC833** | **Internet Engineering** | **10EC836** | **Fuzzy Logic** |

Electives -5(Group E)

|  |  |  |  |
| --- | --- | --- | --- |
| **10EC841** | **Multimedia Communication** | **10EC844** | **Wireless Sensor Networks** |
| **10EC842** | **Real Time Operating Systems** | **10EC845** | **Adhoc Wireless Networks** |
| **10TE843** | **Modeling & Simulation of Data Networks** | **10EC846** | **Optical Computing** |

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